

Multilayer Ceramic Chip Capacitors

Product Top Page

Search by Part No.

Search by Characteristics

Cross Reference

Catalog

Tech Note

Technical Support Tool

FAQ

C5750X7R1H685K250KA



TDK item description ? C5750X7R1H685KT****

Applications



Commercial Grade

Please refer to Part No. [CGA9P2X7R1H685K250KA](#) for Automotive use.

Feature

General

General (Up to 50V)

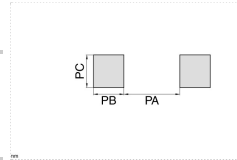
Series

C5750 [EIA 2220]

Status



Production (Not Recommended for New Design)



Images are for reference only and show exemplary products.

PDF file of this page

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Documents

- Catalog
- Specification
- RoHS Certificate
- SVHC/REACH Certificate
- Characterization Sheet
- Promotion Video
- Sample Kits

Technical Support Tools

- S-parameter
- SPICE Netlist (Simple)
- SPICE Netlist (Precision)
- Equivalent Circuit Model
- DC Bias/Superimposition Model (HSPICE)

Pages related to the series to which this product belongs

- Description of icons | Environment

Size

Length(L)	5.70mm ±0.40mm
Width(W)	5.00mm ±0.40mm
Thickness(T)	2.50mm ±0.30mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	
Recommended Land Pattern (PA)	4.10mm to 4.80mm
Recommended Land Pattern (PB)	1.20mm to 1.40mm
Recommended Land Pattern (PC)	4.00mm to 5.00mm

Electrical Characteristics

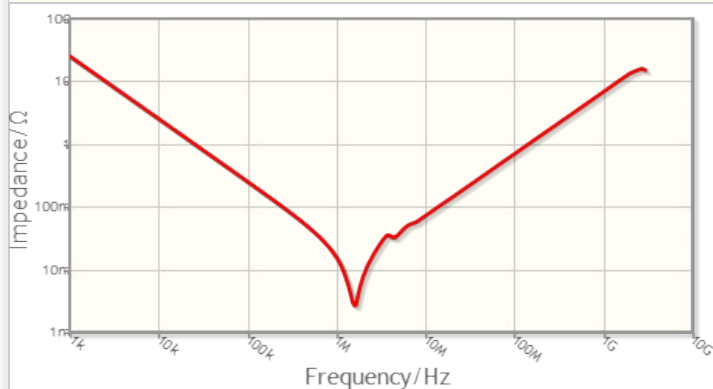
Capacitance	6.8μF ±10%
Rated Voltage	50VDC
Temperature Characteristic ?	X7R(±15%)
Dissipation Factor (Max.)	3%
Insulation Resistance (Min.)	73MΩ

Other

Soldering Method	Reflow
AEC-Q200	No
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	500pcs

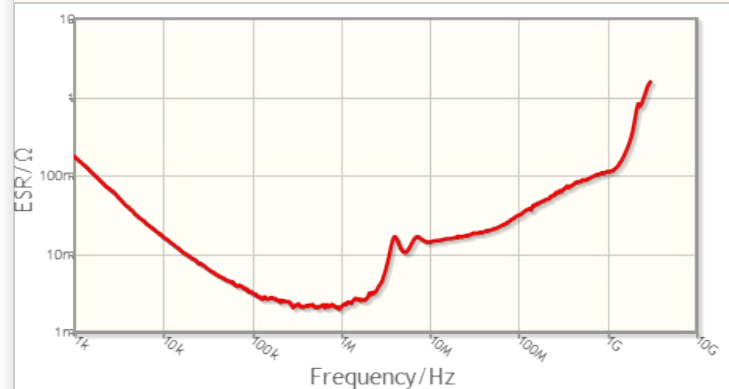
Characteristic Graph (This is reference data, and does not guarantee the products characteristics.)

Impedance



C5750X7R1H685K250KA

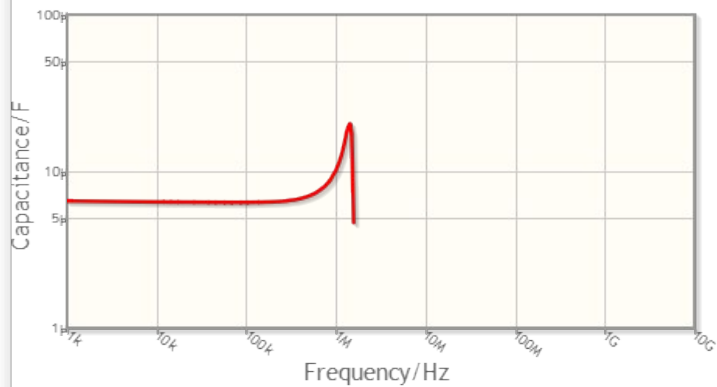
ESR



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Capacitance

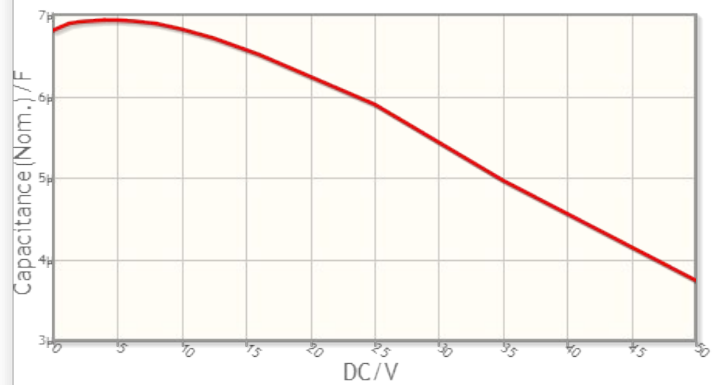


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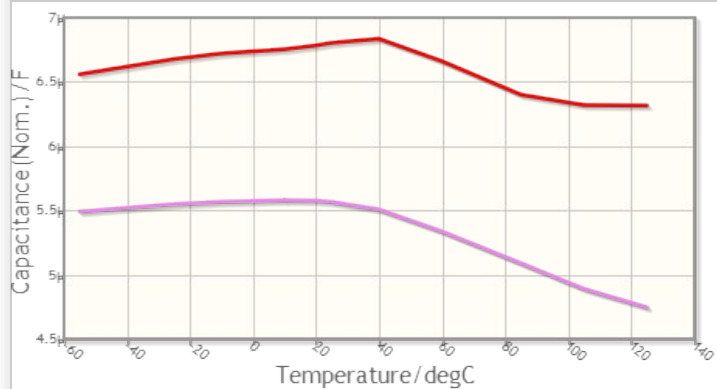
DC Bias Characteristic



C5750X7R1H685K250KA

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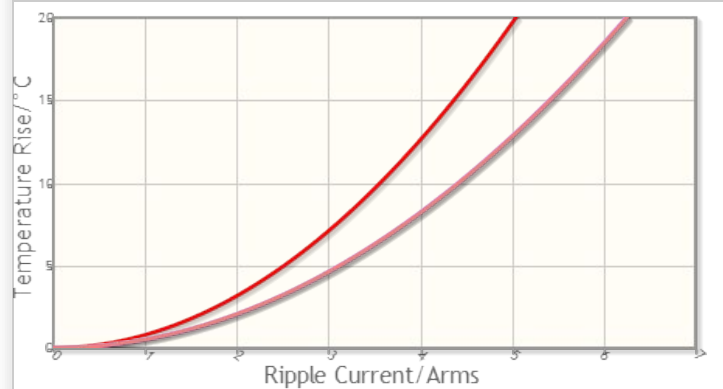
Temperature Characteristic



C5750X7R1H685K250KA(No Bias)
C5750X7R1H685K250KA(DC Bias = 25V)

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Ripple Temperature Rising



C5750X7R1H685K250KA(100k Hz)
C5750X7R1H685K250KA(500k Hz)
C5750X7R1H685K250KA(1M Hz)

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